



# P-Channel 1.8-V (G-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
	0.048 at V <sub>GS</sub> = - 4.5 V	- 3.6		
- 12	0.062 at V <sub>GS</sub> = - 2.5 V	- 3.2		
	0.090 at V <sub>GS</sub> = - 1.8 V	- 2.7		

## **FEATURES**

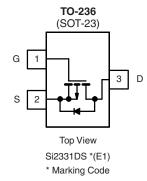
- Halogen-free Option Available
- TrenchFET® Power MOSFETS



RoHS

## **APPLICATIONS**

- Load Switch
- PA Switch



Ordering Information: Si2331DS-T1-E3 (Lead (Pb)-free)

Si2331DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	A = 25 °C, unle	ss otherwise i	noted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 12		V	
Gate-Source Voltage		V <sub>GS</sub>	± 8			
Continuous Dusin Comment /T 150 00\8	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 3.6	- 3.2		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 2.9	- 2.6		
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	- 12		Α	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 0.74	- 0.59		
Down Dischart and	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	0.89	0.71	W	
Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		0.57	0.45	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manifestor Location to Applicated	t ≤ 5 s	- R <sub>thJA</sub>	115	140		
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		140	175	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	$R_{thJF}$	60	75		

#### Notes:

a. Surface Mounted on FR4 board.

b. t ≤ 5 s.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm.

## **Si2331DS**

# Vishay Siliconix



<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °	C, unless o	therwise noted					
Parameter	Symbol		Limits				
		Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = -10 \mu\text{A}$	- 12			V	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \mu A$	- 0.45		- 0.90		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current		V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V			- 1		
	IDSS	$V_{DS} = -12 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10	μΑ	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -5 V$ , $V_{GS} = -4.5 V$	- 6			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -3.6 \text{ A}$		0.038	0.048	Ω	
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -3.2 \text{ A}$		0.049	0.062		
		$V_{GS} = -1.8 \text{ V}, I_D = -2.7 \text{ A}$		0.070	0.090		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -5 \text{ V}, I_D = -3.6 \text{ A}$		3		S	
Diode Forward Voltage	$V_{SD}$	I <sub>S</sub> = - 1.6 A, V <sub>GS</sub> = 0 V			- 1.2	V	
Dynamic <sup>b</sup>	<u>'</u>			•	I.		
Total Gate Charge	$Q_g$			9	14	nC	
Gate-Source Charge	$Q_{gs}$	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}$ $I_{D} \cong -3.6 \text{ A}$		1.3			
Gate-Drain Charge	$Q_{gd}$	1D = - 3.0 A		2.5			
Input Capacitance	C <sub>iss</sub>			780		pF	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		290			
Reverse Transfer Capacitance	C <sub>rss</sub>			210			
Switching <sup>b</sup>				ı			
Turn On Time	t <sub>d(on)</sub>	V 0V D 00		20	30		
Turn-On Time	t <sub>r</sub>	$V_{DD} = -6 \text{ V}, R_L = 6 \Omega$ $I_D \cong -1.0 \text{ A}, V_{GEN} = -4.5 \text{ V}$		35	55	no	
Time Off Time	t <sub>d(off)</sub>	$R_{G} = 6 \Omega$		65	100	ns -	
Turn-Off Time	t <sub>f</sub>	g = 0 32		50	75		

#### Notes:

- a. For DESIGN AID ONLY, not subject to production testing.
- b. Pulse test: PW  $\leq$  300  $\mu$ s duty cycle  $\leq$  2 %.
- c. Switching time is essentially independent of operating temperature.

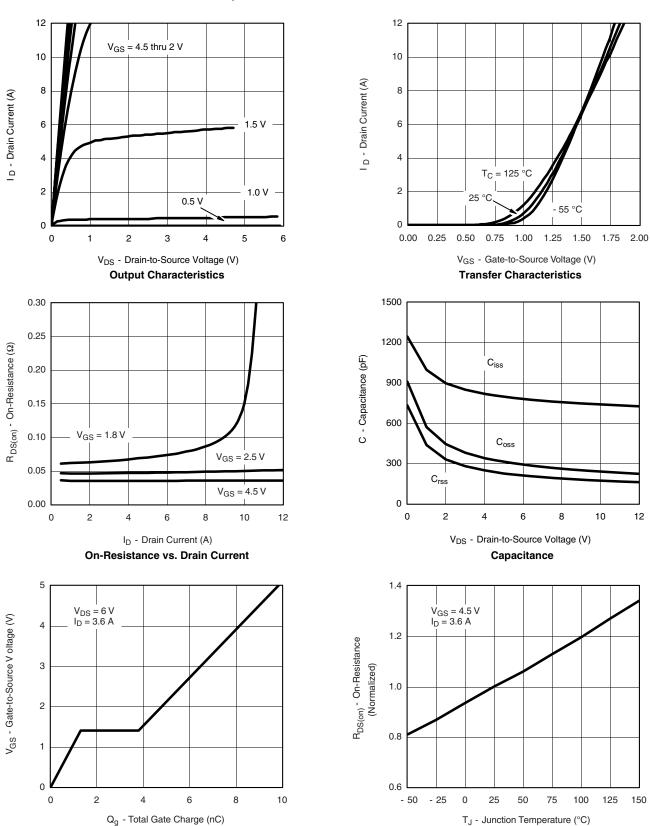
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.







## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

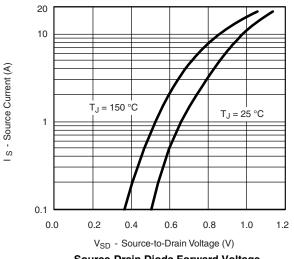


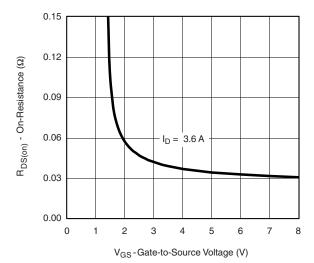
**Gate Charge** 

On-Resistance vs. Junction Temperature

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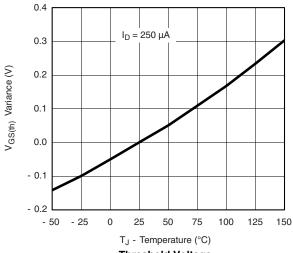
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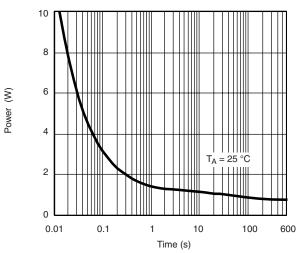




### Source-Drain Diode Forward Voltage

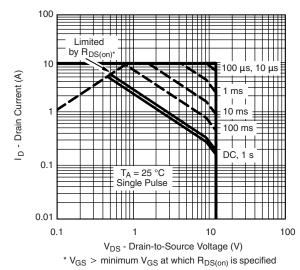






**Threshold Voltage** 

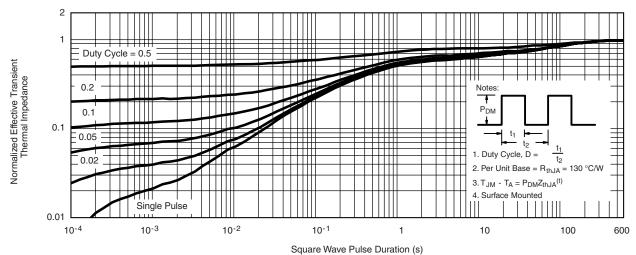
Single Pulse Power







## TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

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